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Patent

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Thomas R. Bayerl, et al.
Serial No.: 09/745,919
Filed: December 21, 2000
For: Use of Bus Hold to Prevent Bus Contention
Between High Speed Processor and
Slow Peripheral

§
§ Group Art Unit: 2181
§
§ Examiner: Dang, Khanh NMN
§
§ Confirmation No.: 6827
§
§
§

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Edith S. Shek
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LETTER TO THE OFFICIAL DRAFTSPERSON

Attached please find formal drawings for this application, consisting of one (1) sheet, Figures 1 to 2, to replace the informal drawings originally filed. Each sheet of drawings indicates the identifying indicia suggested in §1.84(c) on the front of the drawing.

Respectfully submitted,

CONLEY ROSE, P.C.

Date: 4-5-05

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